

WHAT IS CLAIMED IS:

- 1 1. A strained channel semiconductor device comprising:
2 an active area having a length delineated by two opposed termini and a width
3 delineated by two opposed sides;
4 a channel in the active area between the sides thereof, wherein the active area and
5 the channel comprise a residual lengthwise stress; and
6 a first deformation in one side of the active area for selectively enhancing or
7 reducing the residual lengthwise stress in the channel by producing additional lengthwise
8 tensile or compressive force in the active area.
- 1 2. The strained channel semiconductor device of Claim 1, wherein the selective
2 enhancement or reduction of the residual lengthwise stress in the channel selectively
3 affects carrier mobility therein.
- 1 3. The strained channel semiconductor device of Claim 1, wherein there is also
2 residual widthwise compressive stress in the active area and the channel, and wherein the
3 first deformation selectively enhances the residual widthwise stress in the channel by
4 producing additional widthwise compressive force in the active area.
- 1 4. The strained channel semiconductor device of Claim 3, wherein the selective
2 enhancement of the widthwise compressive stress in the channel retards diffusion into the
3 channel of elements used in doping and siliciding the strained channel semiconductor
4 device.

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1 5. The strained channel semiconductor device of Claim 3, wherein a shape of the
2 first deformation is selected to produce additional widthwise compressive stress in the
3 active area and in the channel.

1 6. The strained channel semiconductor device of Claim 1, wherein a shape of the
2 first deformation is selected to produce additional lengthwise tensile or compressive
3 stress in the active area and in the channel.

1 7. The strained channel semiconductor device of Claim 1, wherein the first
2 deformation is a depression in the one side of the active area and the additional
3 lengthwise force is tensile.

1 8. The strained channel semiconductor device of Claim 1, wherein the first
2 deformation is an outward protrusion of the one side of the active area and the additional
3 lengthwise force is compressive.

1 9. The strained channel semiconductor device of Claim 1, further comprising a
2 second deformation in the other side of the active area for selectively further enhancing
3 or decreasing the lengthwise stress in the channel by producing additional lengthwise
4 tensile or compressive force in the active area.

1 10. The strained channel semiconductor device of Claim 9, wherein both
2 deformations are depressions in their respective sides and the additional lengthwise force
3 in the active area is tensile.

1 11. The strained channel semiconductor device of Claim 9, wherein both
2 deformations are protrusions of their respective sides and the additional lengthwise force
3 in the active area is compressive.

1 12. The strained channel semiconductor device of Claim 1, further comprising a
2 second deformation in the one side of the active area and being spaced lengthwise from
3 the first deformation for selectively further enhancing the lengthwise tensile or
4 compressive force in the active area by selectively producing additional lengthwise
5 compressive or tensile force in the active area.

1 13. The strained channel semiconductor device of Claim 12, wherein the first and
2 second deformations are depressions in the one side of the active area and delineate
3 therebetween a third deformation that is a protrusion of the one side of the active area, the
4 first and second deformations producing additional tensile force lengthwise of the
5 channel, the third deformation producing additional compressive force lengthwise of the
6 channel, and all of the deformations producing additional compressive force widthwise of
7 the channel.

1 14. The strained channel semiconductor device of Claim 12, wherein the first and
2 second deformations are protrusions of the one side of the active area and delineate
3 therebetween a third deformation that is a depression in the one side of the active area,
4 the first and second deformations producing additional compressive force lengthwise of
5 the channel, the third deformation producing additional tensile force lengthwise of the
6 channel, and all of the deformations producing additional compressive force widthwise of
7 the channel.

1 15. The strained channel semiconductor device of Claim 12, further comprising third
2 and fourth deformations in the other side of the active area that are selectively aligned or
3 misaligned with the first and second deformations in the one side of the active area for
4 further selectively producing tensile or compressive force lengthwise of the channel and
5 for producing additional compressive force widthwise of the channel.

1 16. The strained channel semiconductor device of Claim 15, wherein the spacing
2 between the first and second deformations and the spacing between the third and fourth
3 deformations and the positions of the first and second deformations relative to the
4 positions of the third and fourth deformations lengthwise of the channel are selected to
5 produce selected stress in one or more selected locations of the channel.

1 17. A method of selectively enhancing or reducing carrier mobility in a strained
2 channel semiconductor device, said method comprising:
3 forming an active area having a length delineated by two opposed termini and a
4 width delineated by two opposed sides;
5 forming a channel in the active area between the sides thereof, wherein the active
6 area and the channel comprise a residual lengthwise stress; and
7 forming a first deformation in one side of the active area to selectively enhance or
8 reduce the lengthwise stress in the channel by producing additional lengthwise tensile or
9 compressive force in the active area.

1 18. The method of Claim 17, wherein the active area and the channel comprise a
2 residual widthwise compressive stress, and wherein the first deformation selectively
3 enhances the widthwise stress in the channel by producing additional widthwise
4 compressive force in the active area.

1 19. The method of Claim 18, wherein the selective enhancement of the widthwise
2 compressive stress in the channel retards diffusion into the channel of elements used in
3 doping and siliciding the strained channel semiconductor device.

1 20. The method of Claim 17, wherein the shape of the first deformation is selected to
2 produce selected additional lengthwise tensile or compressive stress in the active area and
3 in the channel.

1 21. The method of Claim 17, wherein the shape of the first deformation is selected to
2 produce selected additional widthwise compressive stress in the active area and in the
3 channel.

1 22. The method of Claim 17, wherein the first deformation is a depression in the one
2 side of the active area and the additional lengthwise stress is tensile.

1 23. The method of Claim 17, wherein the first deformation is an outward protrusion
2 of the one side of the active area and the additional lengthwise stress is compressive.

1 24. The method of Claim 17, further comprising forming a second deformation in the
2 other side of the active area to selectively further enhance or decrease the lengthwise
3 stress in the channel by producing additional lengthwise tensile or compressive force in
4 the active area.

1 25. The method of Claim 24, wherein both deformations are depressions in their
2 respective sides and the additional lengthwise force in the channel is tensile.

1 26. The method of Claim 24, wherein both deformations are protrusions of their
2 respective sides and the additional lengthwise force in the channel is compressive.

1 27. The method of Claim 17, further comprising forming a second deformation in the
2 one side of the active area, the second deformation being spaced lengthwise from the first
3 deformation, to selectively further enhance the lengthwise tensile or compressive force in
4 the channel by selectively producing additional lengthwise tensile or compressive force
5 in the active area.

1 28. The method of Claim 27, wherein the first and second deformations are
2 depressions in the one side of the active area and delineate therebetween a third
3 deformation that is a protrusion of the one side of the active area, the first and second
4 deformations producing additional tensile force lengthwise of the channel, the third
5 deformation producing additional compressive force lengthwise of the channel, and all of
6 the deformations producing additional compressive force widthwise of the channel.

1 29. The method of Claim 27, wherein the first and second deformations are
2 protrusions of the one side of the active area and delineate therebetween a third
3 deformation that is a depression in the one side of the active area, the first and second
4 deformations producing additional compressive force lengthwise of the channel, the third
5 deformation producing additional tensile force lengthwise of the channel, and all of the
6 deformations producing additional compressive force widthwise of the channel.

1 30. The method of Claim 27, further comprising forming in the other side of the
2 active area third and fourth deformations that are spaced apart lengthwise of the channel
3 to selectively produce additional tensile or compressive force lengthwise of the channel
4 and to produce additional compressive force widthwise of the channel.

1 31. The method of Claim 30, further comprising selecting the spacing between the
2 first and second deformations and the spacing between the third and fourth deformations
3 and selecting the positions of the first and second deformations relative to the positions of
4 the third and fourth deformations lengthwise of the channel to produce selected stress in
5 one or more selected locations of the channel.

1 32. A method of selectively enhancing or reducing carrier mobility in each of several
2 zones of a channel disposed in an active area of a strained channel semiconductor device
3 fabricated in and on a generally planar semiconductor segment, the method comprising:
4 forming the active area having a length delineated by two opposed termini and a
5 width delineated by two opposed sides;
6 forming the channel in the active area between the sides thereof, wherein the
7 active area and the channel comprise a residual lengthwise stress, wherein the zones lie
8 lengthwise of the channel between the sides; and
9 forming one or more deformations in one or more of the sides of the active area to
10 selectively enhance or reduce the lengthwise stress in selected zones of the channel by
11 producing additional lengthwise tensile or compressive force in the respective zones.